Status Report 12/1/2002

- Fix and update the EDAM net extraction
- Added the via tile removal code
- Added resistor geometry detection and resizing. However, I still need to debug the resizing part.
- Fixed some bugs on the inductor generation code.
- Try to observed SOI technology file, but experienced problem while loading it. The parser is not compatible with the SOI tech file's style/format, and need to be updated. This may require widespread chances in EDAM codes.

Next week plan:
- Fix the design rule problem, which may include updating EDAM scaling formula.
- Check and debug resistor resizing function
- Continue on the inductor equation