

Behavioral group meeting held on November 8, 2002

Present

Yong Wang, Lili Zhou, Erik Normark, Renpi Yu, Chris Baker, Kishore Tanikella,
Gus Mak, Pavel Nikitin

Issues

- Unidentified BMAC compiler problem with simulating A/D converter in Spice using compiled models
- Boeing design has a truncated netlist making simulation not possible
- DAC paper:
 - To write or not to write?
 - The content – modeling or compiler?
 - The technical depth of the content?

Future work plan

- Continue with EKV modeling (Chris, Gus)
- Resolve BMAC compiler problems that do not allow to model A/D converter (Kishore's thesis project)
- Continue on the learning curve (Erik, Renpi)
- Resolve computing issues (accounts, computers, planned move)
- Discuss with Richard:
 - Using Kishore's A/D converter design instead of Boeing's
 - DAC paper